



ITW

PATENT
8008-1050

IN THE U.S. PATENT AND TRADEMARK OFFICE

In re application of

Hiroyoshi KUGE et al.

Conf. 3414

Application No. 10/786,552

Group 2825

Filed February 26, 2004

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
AND DESIGN AUTOMATION APPARATUS, METHOD
AND PROGRAM

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

May 5, 2005

Sir:

In compliance with Rules 1.97 and 1.98, and in fulfillment of the duty of disclosure under Rule 1.56, the cited documents are made of record on the enclosed PTO Form-1449.

A concise explanation of the relevance of these items is that these references were cited by the Japanese Patent Office in an Official Action. A copy of the Japanese Official Action in which they were cited is attached hereto, with what is believed to be the pertinent portion enclosed in a wavy line. **An English translation of the enclosed portion is also attached hereto.**

Respectfully submitted,

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